GAL Variante

ECB / Portselect
mit GAL16V

Datei: GAL Variante.kicad_sch

Seriel-6850

Seriell V24
mit 6850
V24 Treiber 75188/189

Datei: Seriel-6850.kicad_sch

Parallel-PIO1

Parallel
PIO1 8255

Datei: Parallel.kicad_sch

PIO-2

RTC, IO-Byte Switch
PIO 2 8255

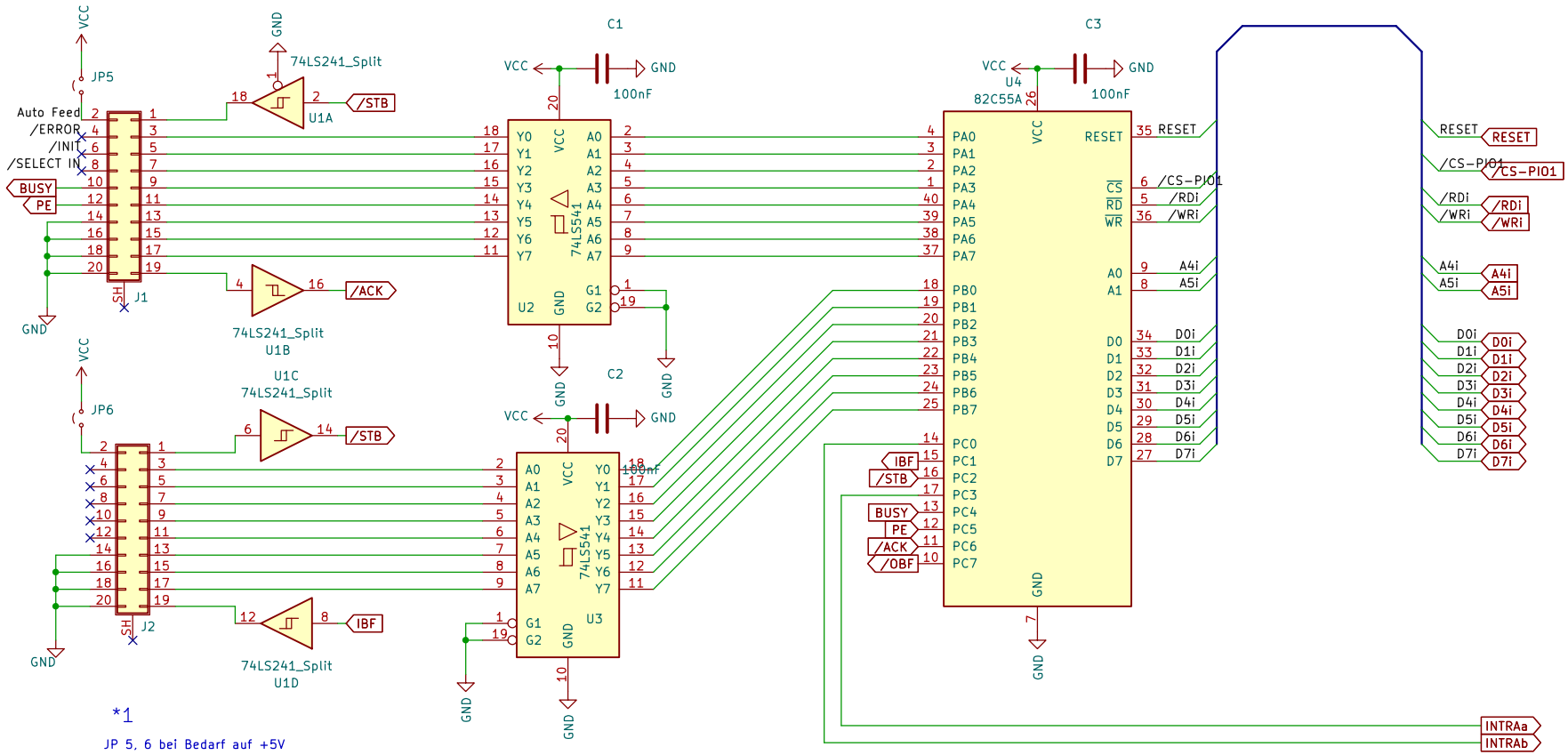
Datei: PIO2.kicad_sch

Multi-IO Übersicht

Sheet: /
File: Multi-IO.kicad_sch

Title: Multi-IO Karte

Size: A4	Date: 2026-09-18	**Rev: V2.0**
KiCad E.D.A. 9.0.5		Id: 1/5



Legende

*1) Stromversorgung für externe Module

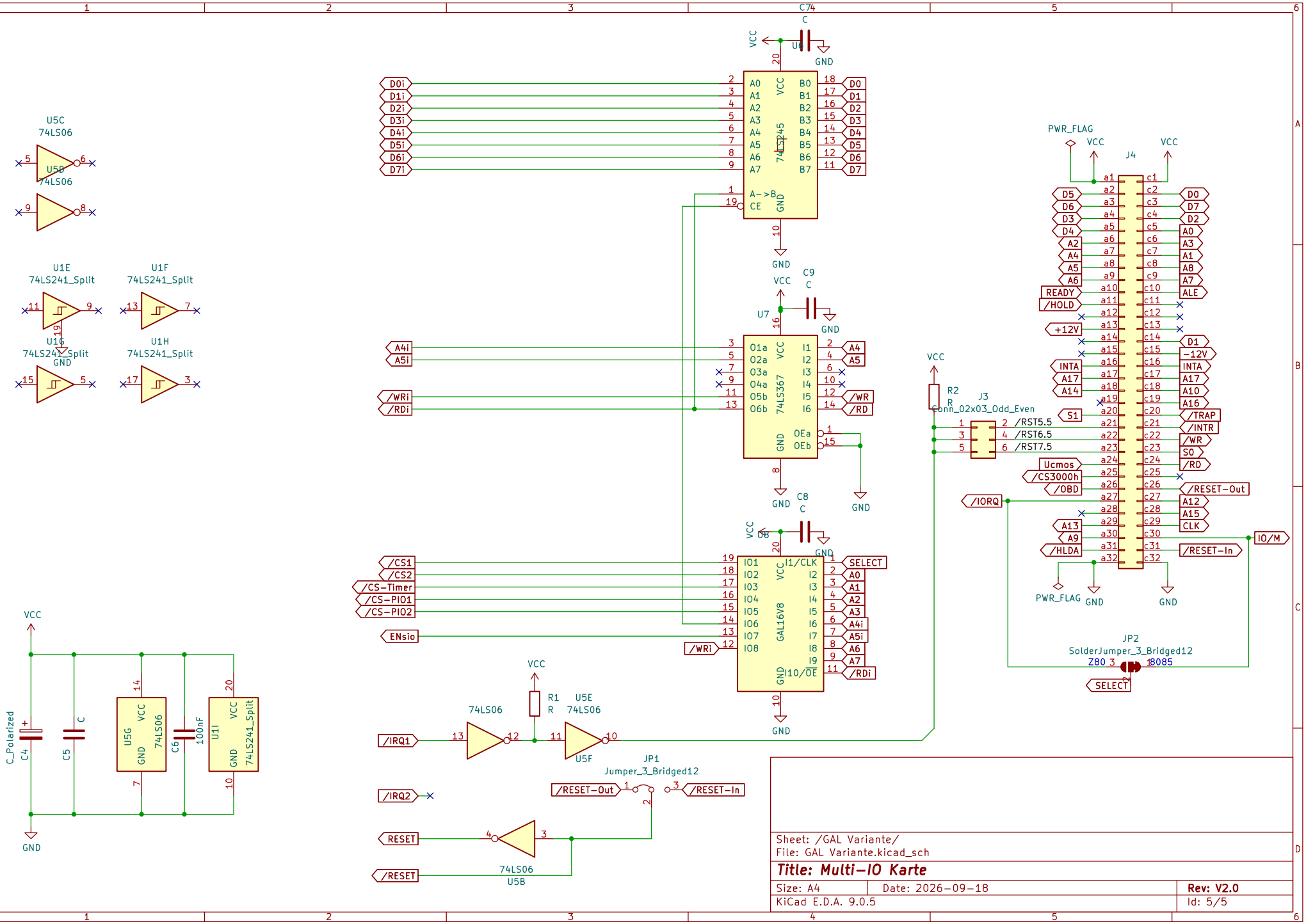
! J11 und J12 sollten gegen 26pol getaucht und die Belegung für Crimpstecker angepasst werden

PIO 1A Centronic
PIO 1B Parallel Eingabe (Tastatur)
PIO 1C Control

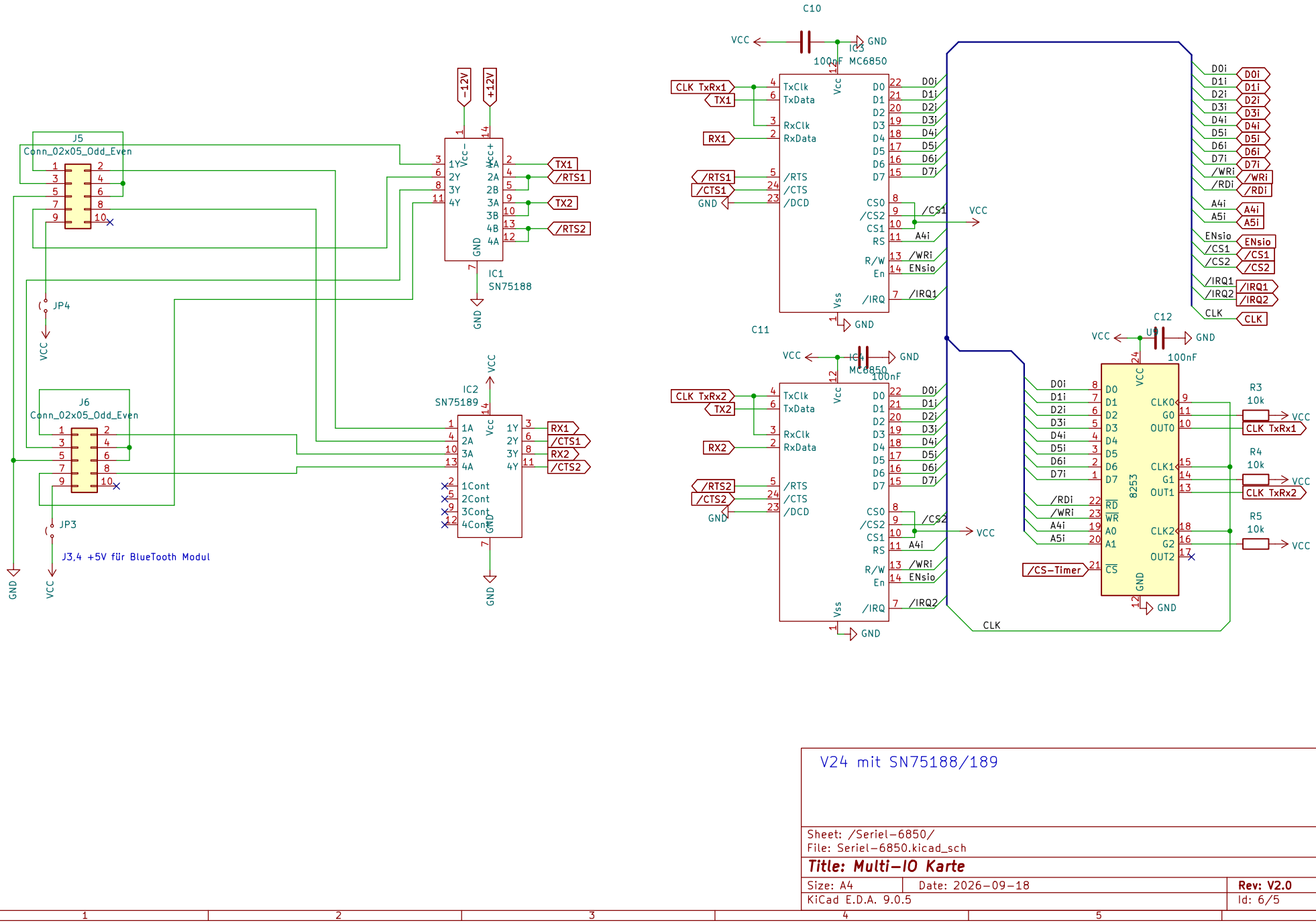
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File: Parallel.kicad_sch

Title: Multi-IO Karte

Size: A4	Date: 2026-09-18	**Rev: V2.0**
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Sheet: /GAL Variante/		
File: GAL Variante.kicad_sch		
Title: Multi-IO Karte		
Size: A4	Date: 2026-09-18	**Rev: V2.0**
KiCad E.D.A. 9.0.5		Id: 5/5

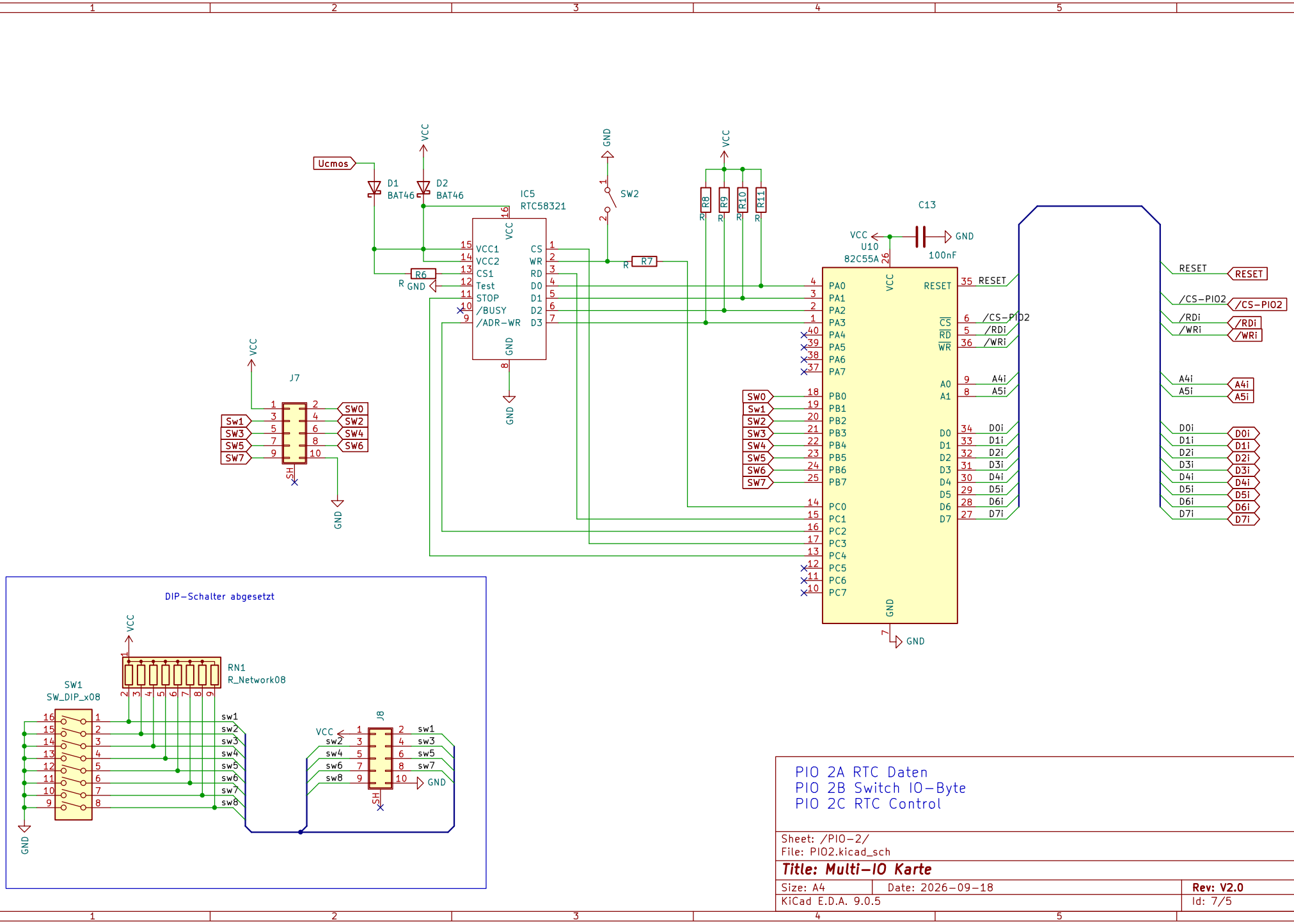


V24 mit SN75188/189

Sheet: /Seriel-6850/
File: Seriel-6850.kicad_sch

Title: Multi-IO Karte

Size: A4	Date: 2026-09-18	**Rev: V2.0**
KiCad E.D.A. 9.0.5		Id: 6/5


Ucmos
D1
BAT46
D2
BAT46
VCC
GND
SW2
R8
R9
R10
R11
C13
100nF
IC5
RTC58321
VCC1
VCC2
CS1
Test
STOP
/BUSY
/ADR-WR
CS
WR
RD
D0
D1
D2
D3
R6
R7
U10
82C55A
PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
PC0
PC1
PC2
PC3
PC4
PC5
PC6
PC7
RESET
/CS-PIO2
/RDi
/WRi
A4i
A5i
D0i
D1i
D2i
D3i
D4i
D5i
D6i
D7i
J7
Sw1
SW3
SW5
SW7
SW0
SW2
SW4
SW6
SH
DIP-Schalter abgesetzt
SW1
SW_DIP_x08
RN1
R_Network08
sw1
sw2
sw3
sw4
sw5
sw6
sw7
sw8
J8
PIO 2A RTC Daten
PIO 2B Switch IO-Byte
PIO 2C RTC Control
Sheet: /PIO-2/
File: PIO2.kicad_sch
Title: Multi-IO Karte
Size: A4
Date: 2026-09-18
Rev: V2.0
KiCad E.D.A. 9.0.5
Id: 7/5