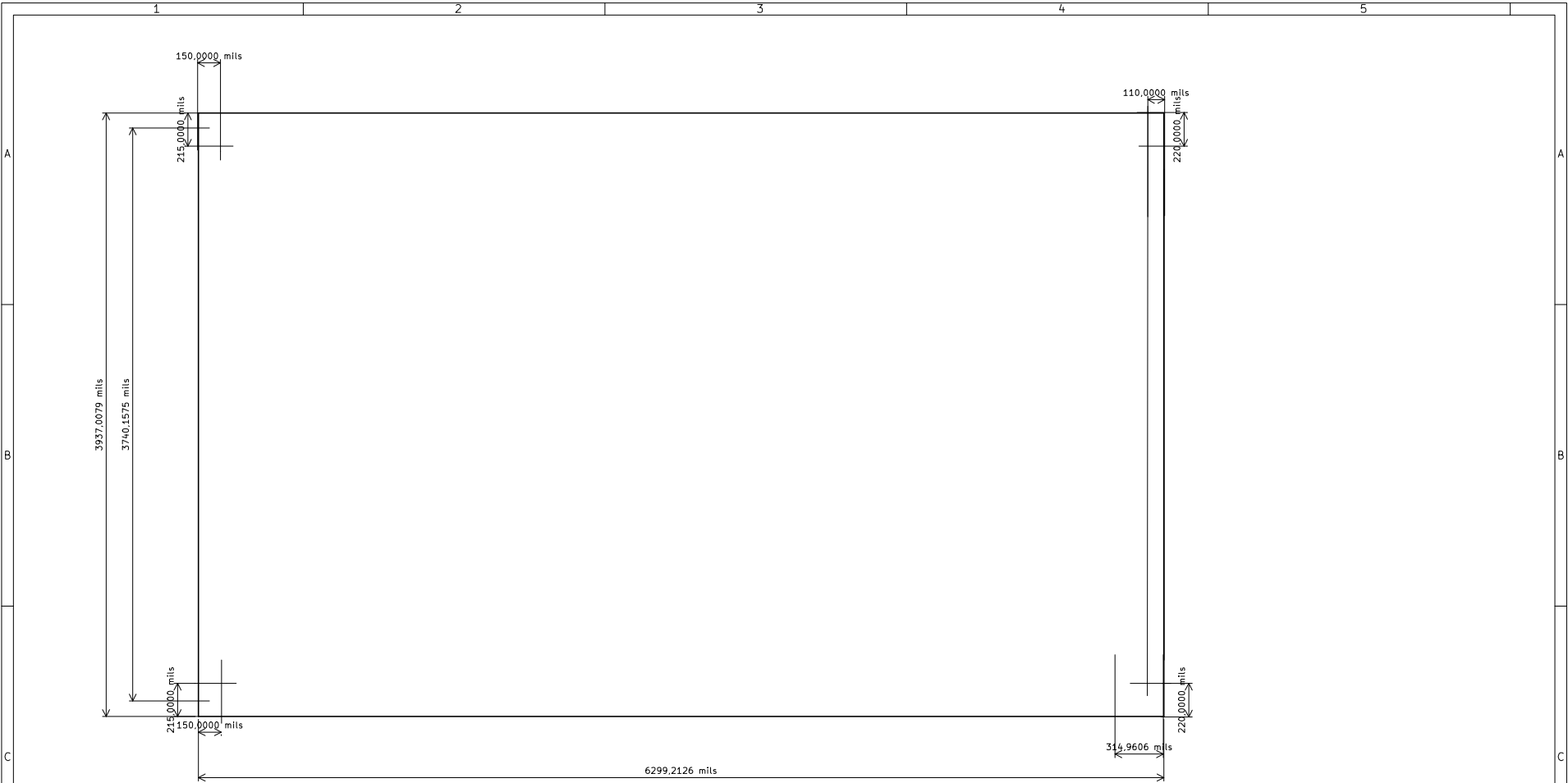




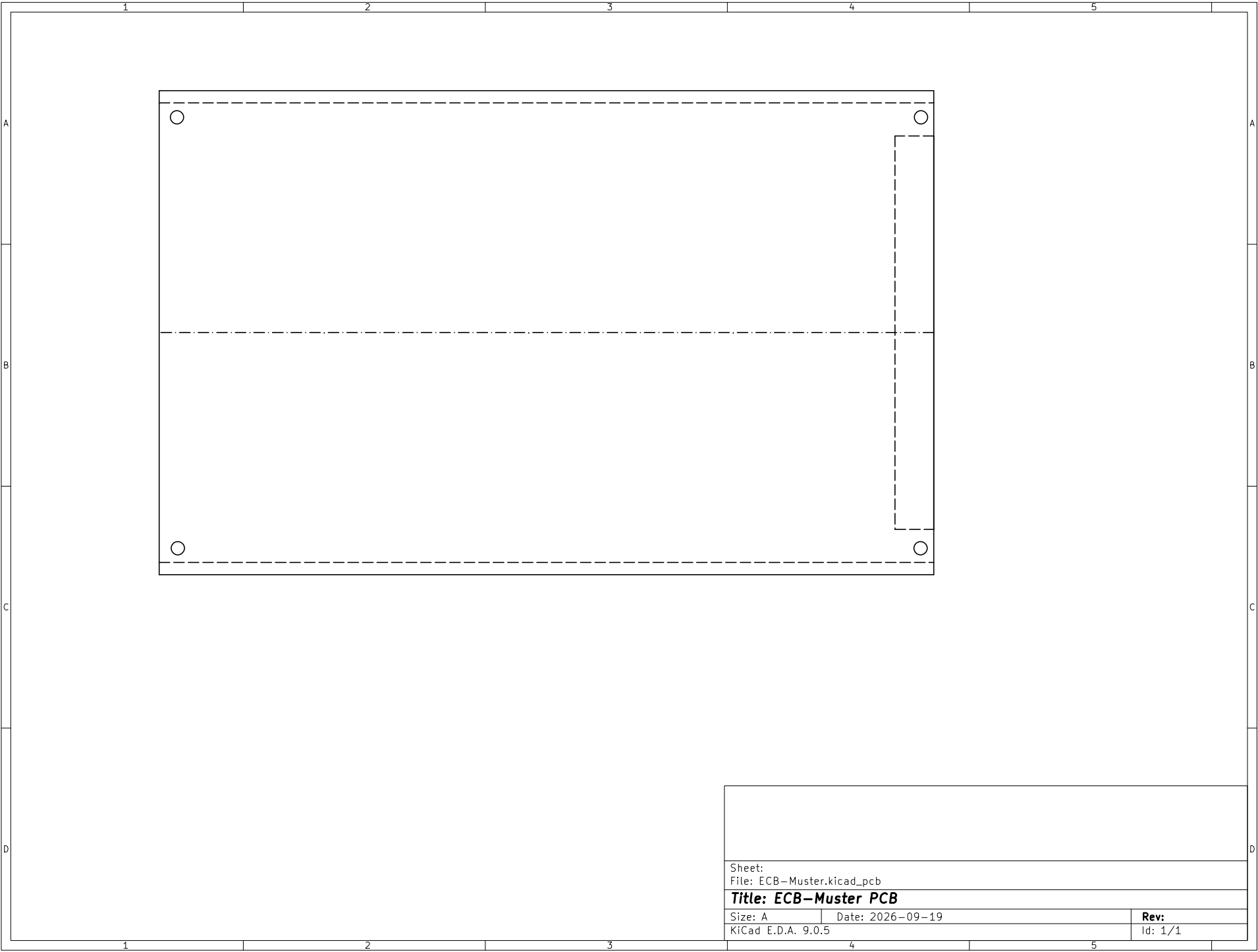
Layer Name	Type	Material	Thickness (mm)	Color	Epsilon R	Loss Tangent
F.Silkscreen	Top Silk Screen	Liquid Photo	0 mm	White	1	0
F.Paste	Top Solder Paste		0 mm		1	0
F.Mask	Top Solder Mask	Liquid Ink	0.01 mm	Green	3.3	0
F.Cu	copper		0.035 mm		1	0
Dielectric 1	prepreg	7628	0.2104 mm	FR4 natural	4.4	0
In1.Cu	copper		0.0152 mm		1	0
Dielectric 2	core	FR4	1.065 mm	FR4 natural	4.6	0.02
In2.Cu	copper		0.0152 mm		1	0
Dielectric 3	prepreg	7628	0.2104 mm	FR4 natural	4.4	0
B.Cu	copper		0.035 mm		1	0
B.Mask	Bottom Solder Mask	Liquid Ink	0.01 mm	Green	3.3	0
B.Paste	Bottom Solder Paste		0 mm		1	0
B.Silkscreen	Bottom Silk Screen	Liquid Photo	0 mm	White	1	0

Note on board design.

This board is designed according to JLCPCB guidance, and the controlled impedance stack-up is JLC04161H-7628.

All controlled impedance traces are either 50Ω single-ended or 90Ω differential pair (with a 50Ω single-ended target).

Sheet:		
File: ECB-Muster.kicad_pcb		
Title: ECB-Muster PCB		
Size: A	Date: 2026-09-19	Rev:
KiCad E.D.A. 9.0.5		Id: 1/1



Sheet:		
File: ECB-Muster.kicad_pcb		
Title: ECB-Muster PCB		
Size: A	Date: 2026-09-19	Rev:
KiCad E.D.A. 9.0.5		Id: 1/1