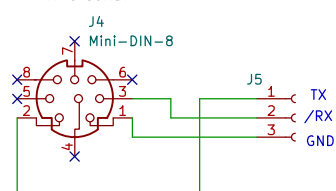
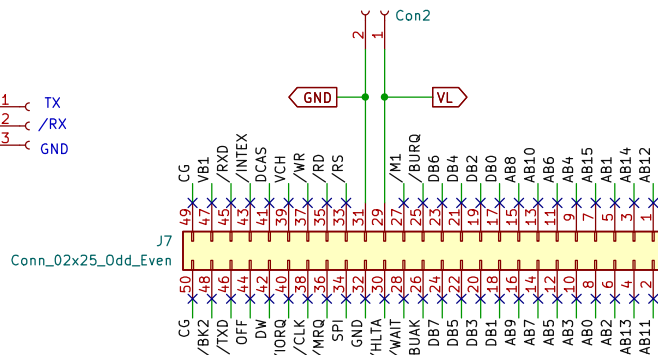


PX-8 Serial



PX-8 Expansionsport
(nur Stromversorgung)



/TXD und /RXD = RS232 mit TTL-Pegel
AB0..15 Adressbus
DB0..7 Datenbus

Table 1-12 CN7 (RS-232C Interface) Pin Assignments

Pin No.	Signal Name	Definition
1	GND	Signal ground
2	TXD	Transmit data (output)
3	RXD	Receiving data (input)
4	RST	Request-to-send (output)
5	CTS	Clear-to-send (input)
6	DSR	Data set ready (output)
7	DTR	Data terminal ready (output)
8	CD	Carrier detect
E	FG	Chassis ground

REV.-A

REV.-A

Table 1-13 CN8 (Expansion) Interface



Pin No.	Signal Name	Direction	Definition	Pin No.	Signal Name	Direction	Definition
1	AB12	O	Address bus 12	2	AB11	O	Address bus 11
3	AB14	O	Address bus 14	4	AB13	O	Address bus 13
5	AB 1	O	Address bus 1	6	AB 2	O	Address bus 2
7	AB15	O	Address bus 15	8	AB 0	O	Address bus 0
9	AB 4	O	Address bus 4	10	AB 3	O	Address bus 3
11	AB 6	O	Address bus 1	12	AB 5	O	Address bus 5
13	AB10	O	Address bus 10	14	AB 7	O	Address bus 7
15	AB 8	O	Address bus 8	16	AB 9	O	Address bus 9
17	DB 0	I/O	Data bus 0	18	DB 1	I/O	Data bus 1
19	DB 2	I/O	Data bus 2	20	DB 3	I/O	Data bus 3
21	DB 4	I/O	Data bus 4	22	DB 5	I/O	Data bus 5
23	DB 6	I/O	Data bus 6	24	DB 7	I/O	Data bus 7
25	BUAQ	O	Bus request	26	BUAK	O	Bus acknowledge
27	M1	O	Machine cycle 1	28	WAIT	I	Wait
29	VL	O	Circuit voltage: +5V	30	HLTA	O	Halt acknowledge
31	GND	—	Signal ground	32	GND	—	Signal ground
33	RS	O	Reset	34	SPI	I	Speaker
35	RD	O	Read	36	MRQ	O	Memory request
37	WR	O	Write	38	CLK	O	2.45 MHz
39	VCH	O	Charging voltage	40	IORQ	O	I/O request
41	DCAS	O	Data CAS	42	DW	O	Data write
43	INTX	I	External interruption	44	OFF	O	Initializing signal of IC "6A"
45	RXD	I	Serial received data	46	TXD	O	Serial send data
47	VB1	O	Battery voltage	48	BK2	I	Bank exchange
49	CG	—	Chassis ground	50	CG	—	Chassis ground

Sheet: /Anschlüsse/
File: untitled.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.5

Rev:
Id: 2/2